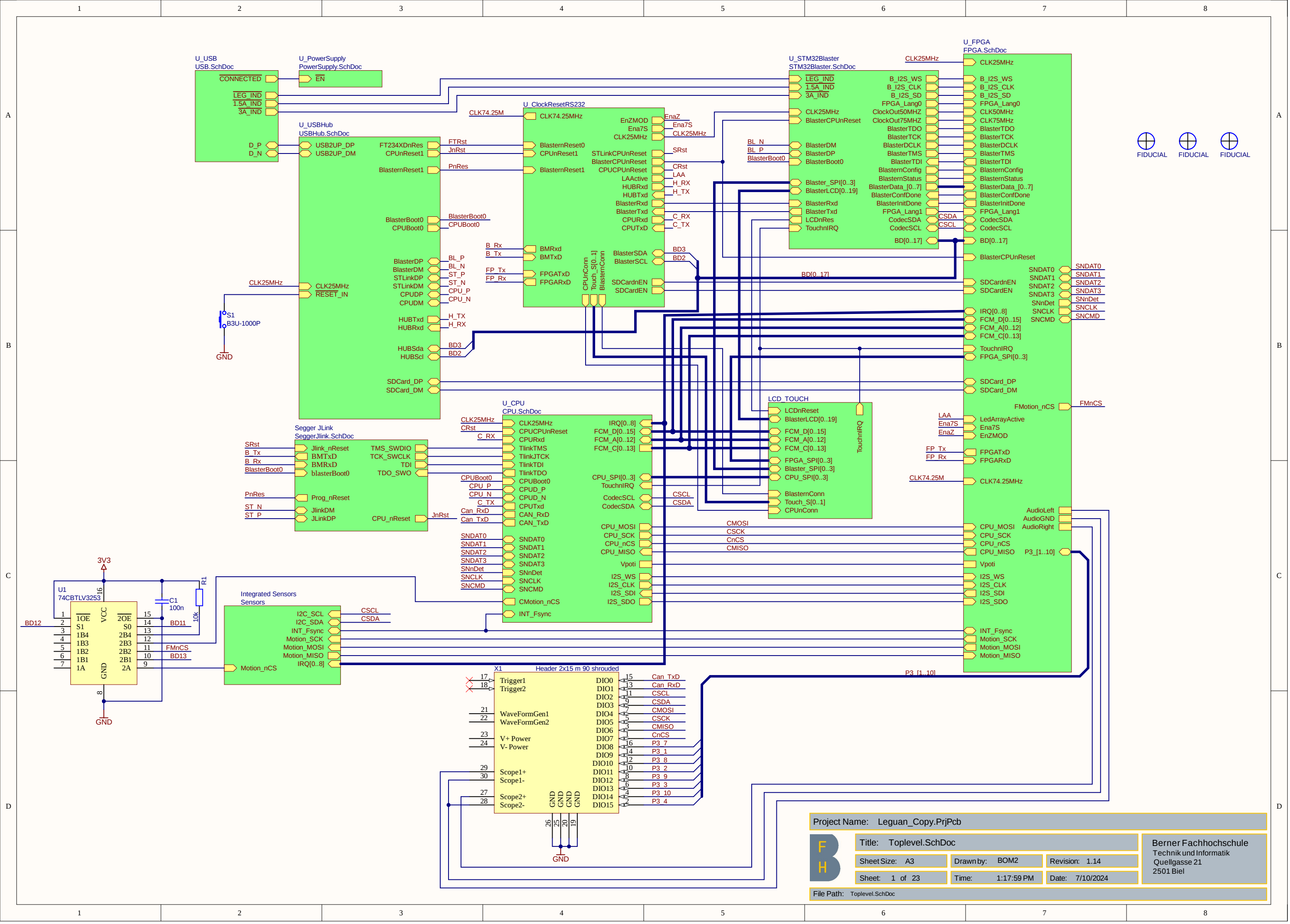
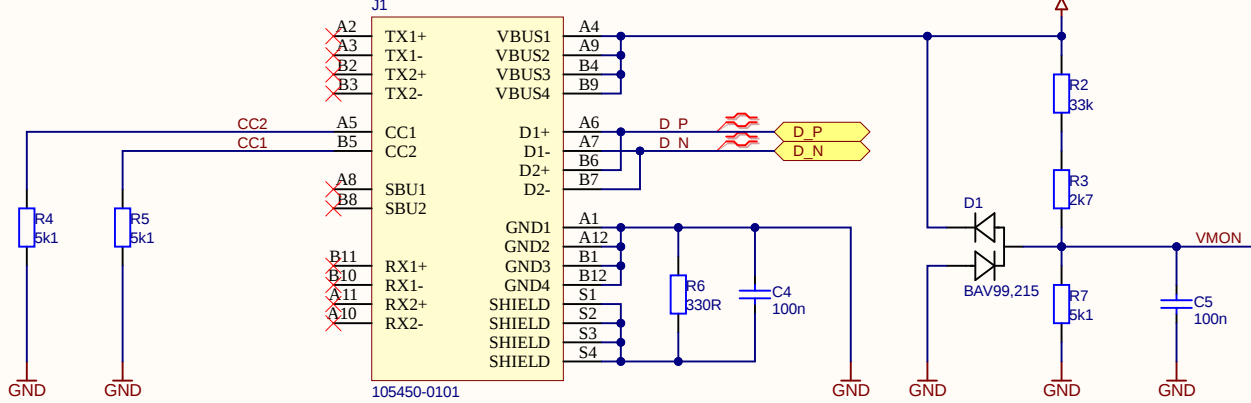
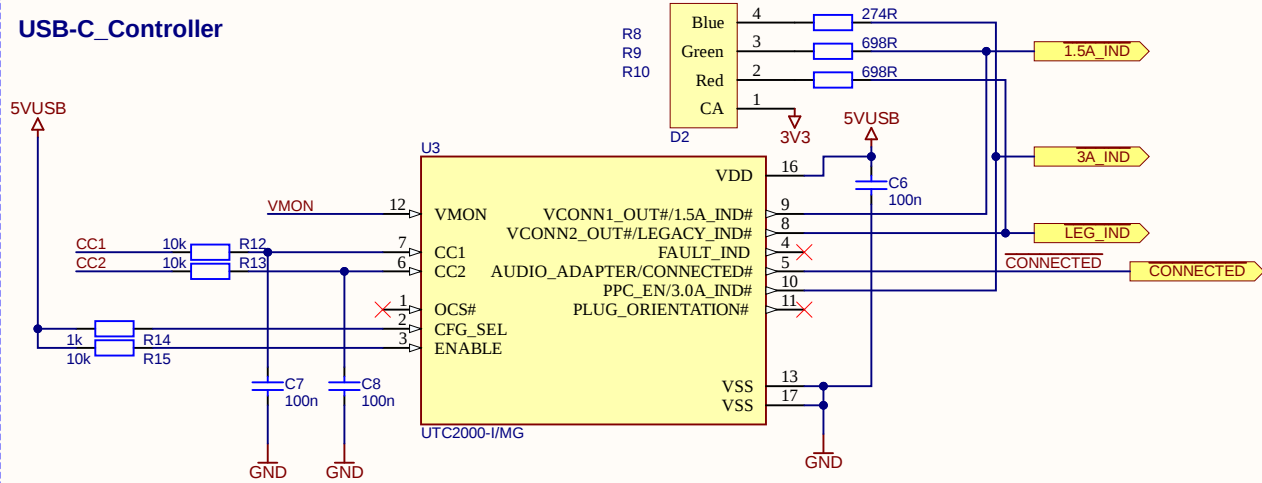




USB-C_Connector



USB-C_Controller



Project Name: Leguan_Copy.PrjPcb



Title: USB.SchDoc

Sheet Size: A4

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Time: 1:17:59 PM

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3400 Burgdorf

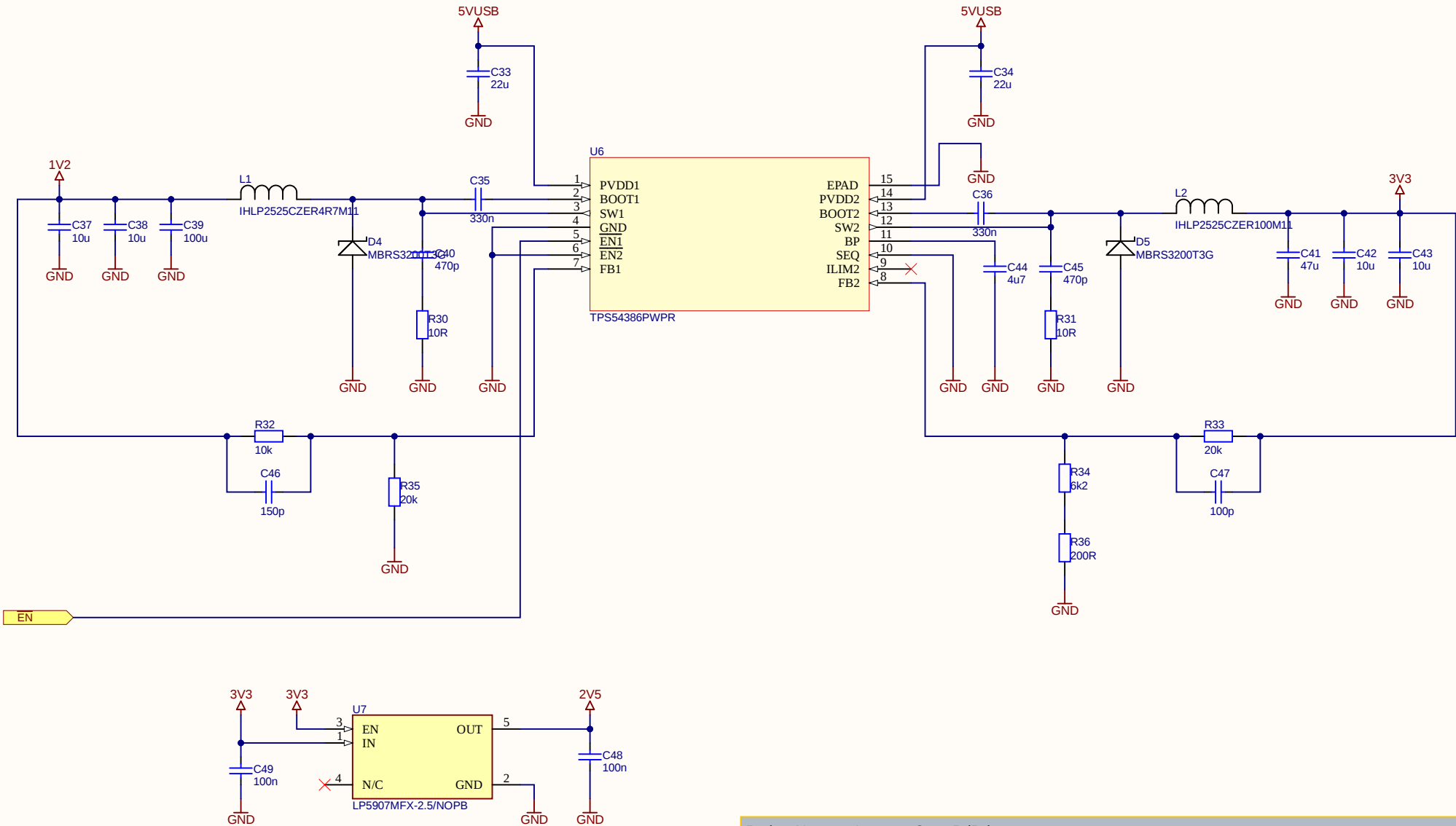
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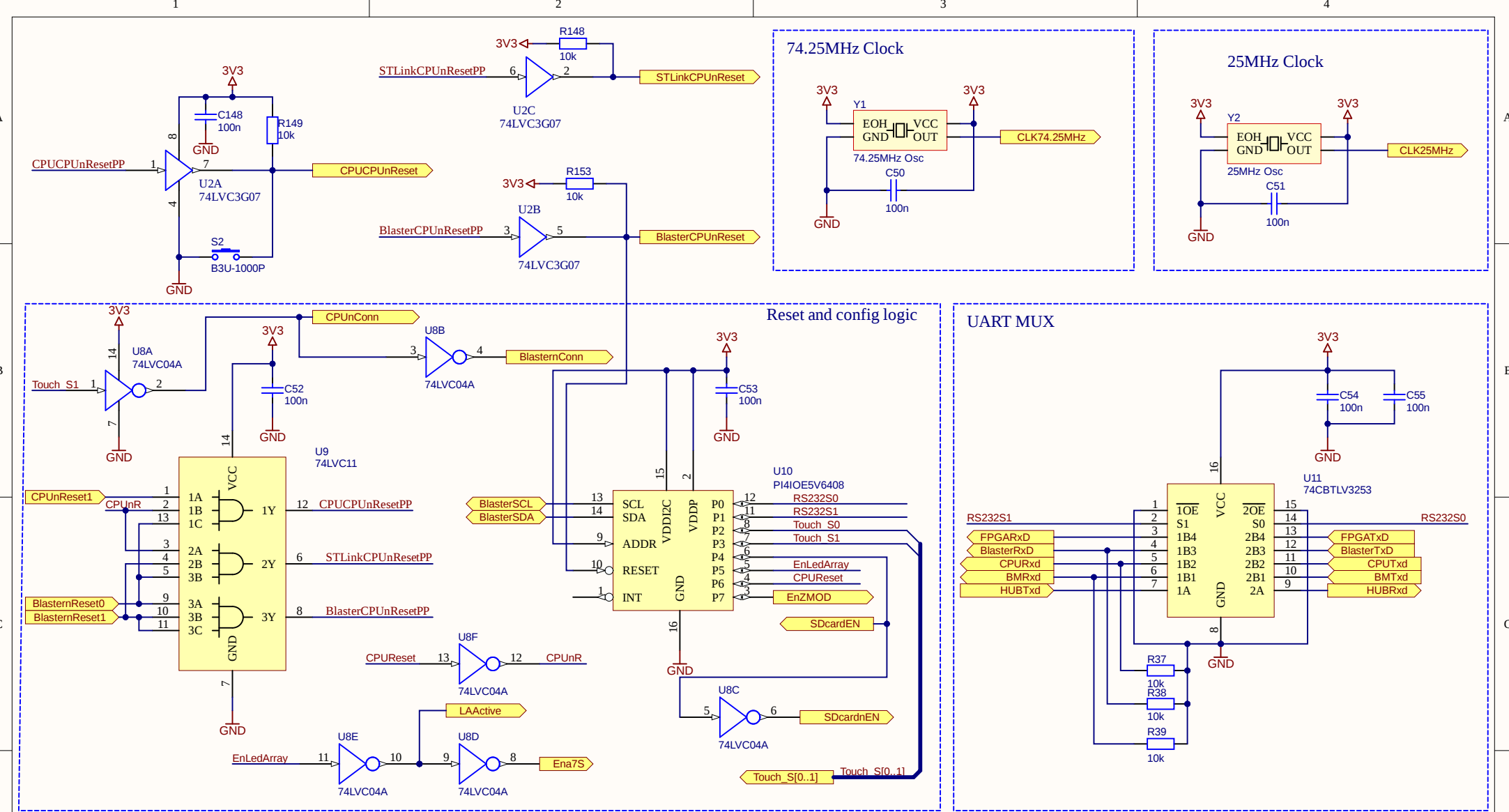
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Project Name: Leguan_Copy.PrfPcb			
F H	Title: PowerSupply.SchDoc		Berner Fachhochschule Technik und Informatik Quellgasse 21 2501 Biel
	Sheet Size: A4	Drawn by: BOM2	
	Sheet: 4 of 22	Time: 1:18:00 PM	
Date: 7/10/2024			
File Path: PowerSupply.SchDoc			



Project Name: Leguan_Copy.PrjPcb



Title: ClockResetRS232.SchDoc

Sheet Size: A4

Drawn by: BOM2

Revision: 1.14

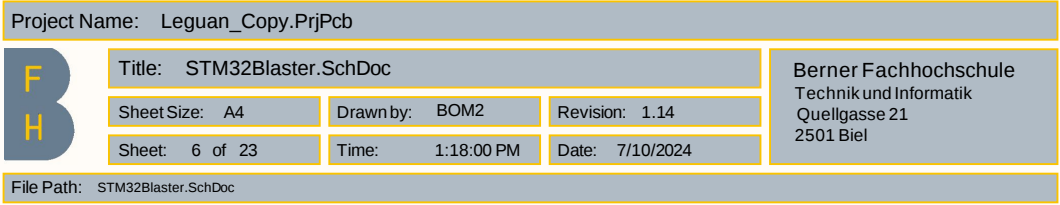
Sheet: 5 of 23

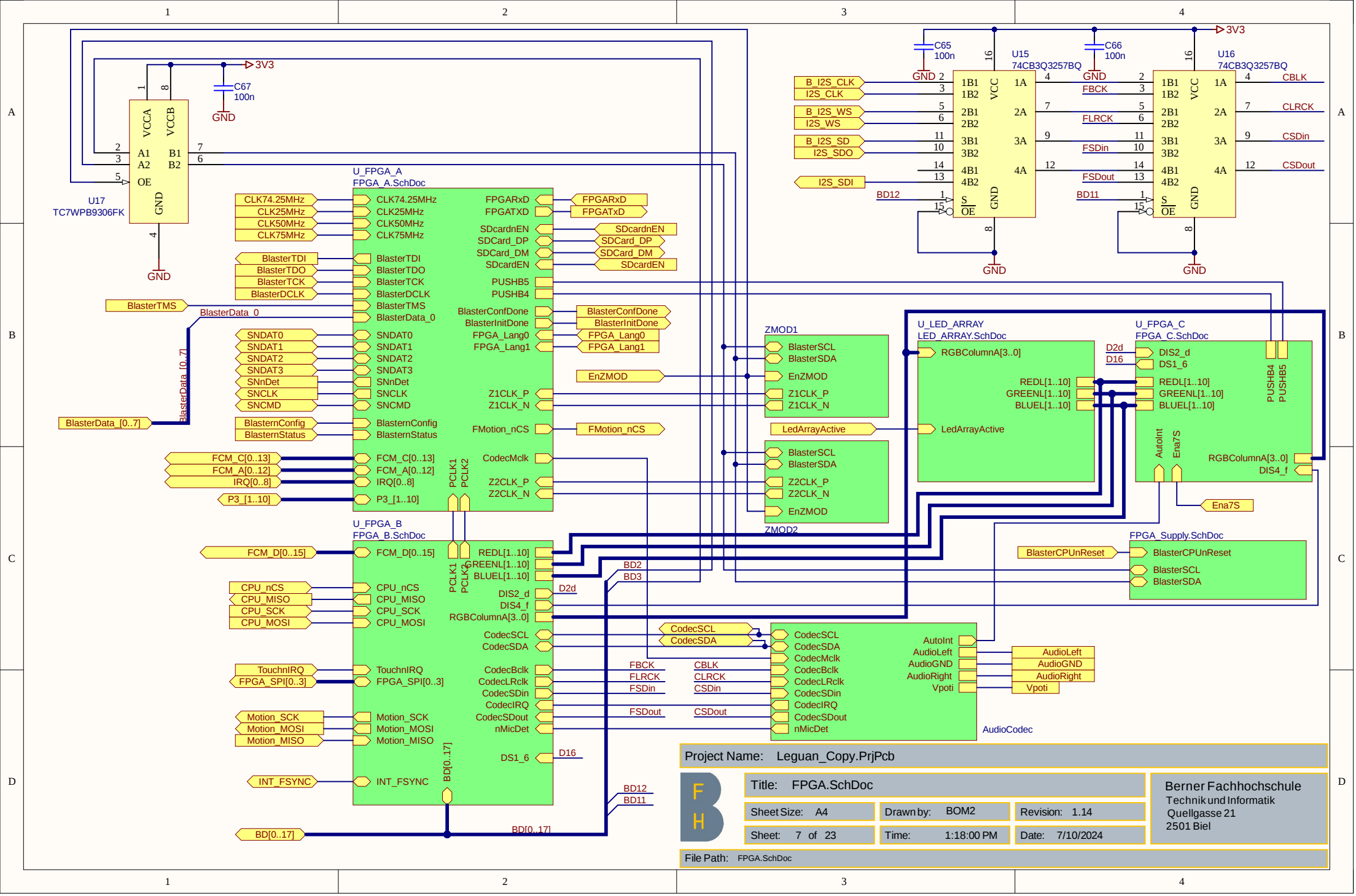
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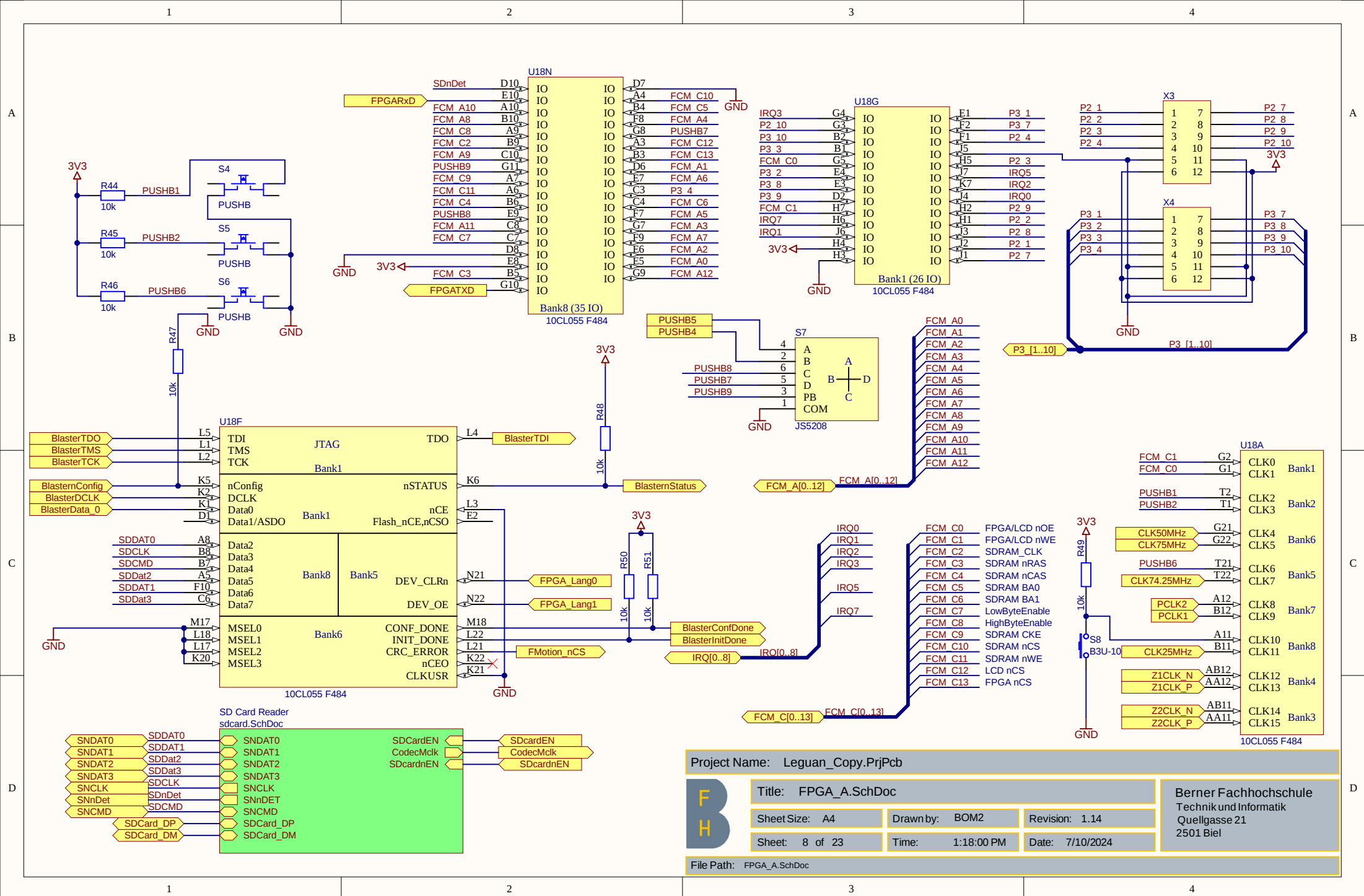
Date: 7/10/2024

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Technik und Informatik
Quellgasse 21
2501 Biel

File Path: ClockResetRS232.SchDoc







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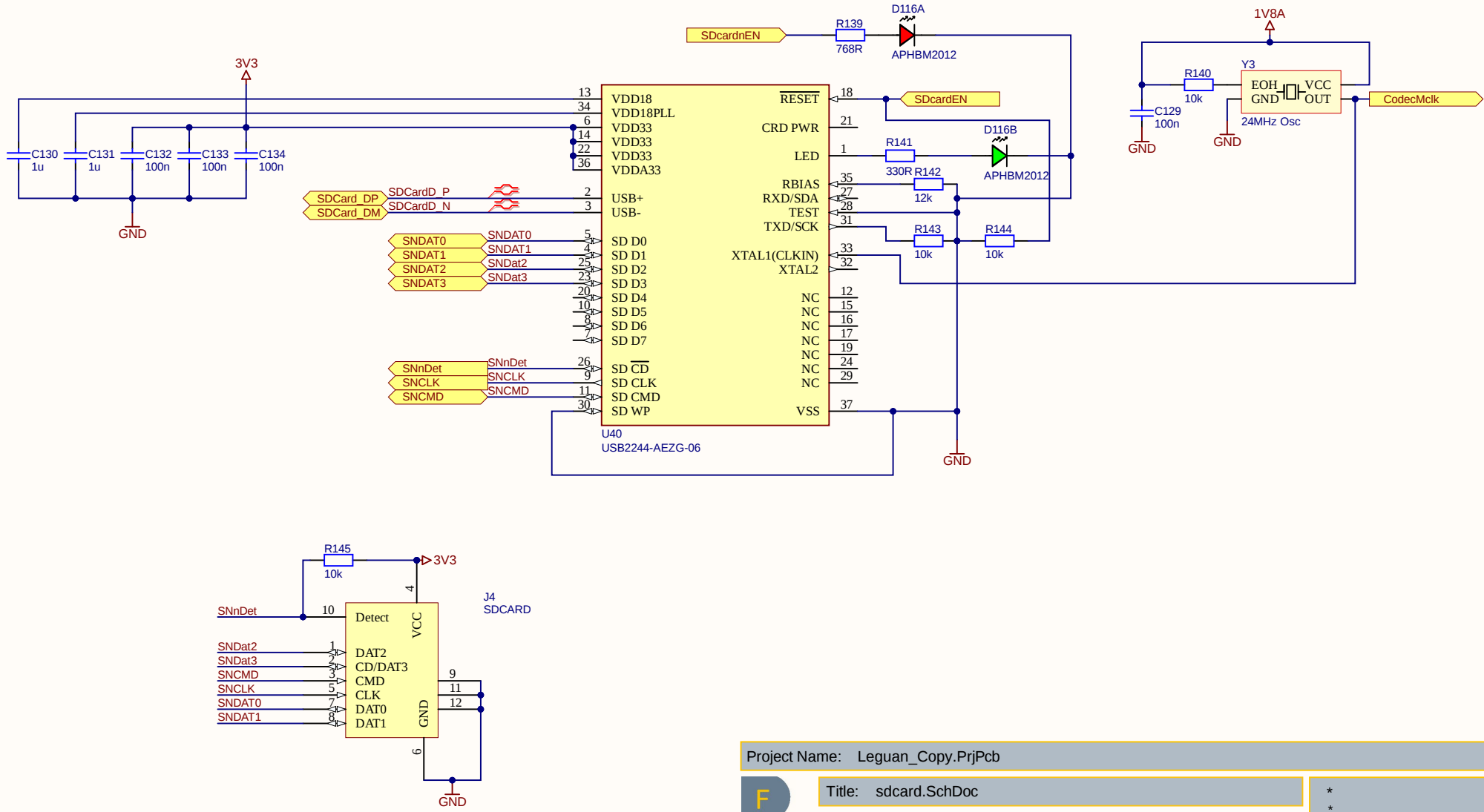
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Project Name: Leguan_Copy.PrfPcb

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Title: sdcard.SchDoc

Sheet Size: A4

Sheet: 9 of 23

Drawn by: ktt1

Time: 1:18:00 PM

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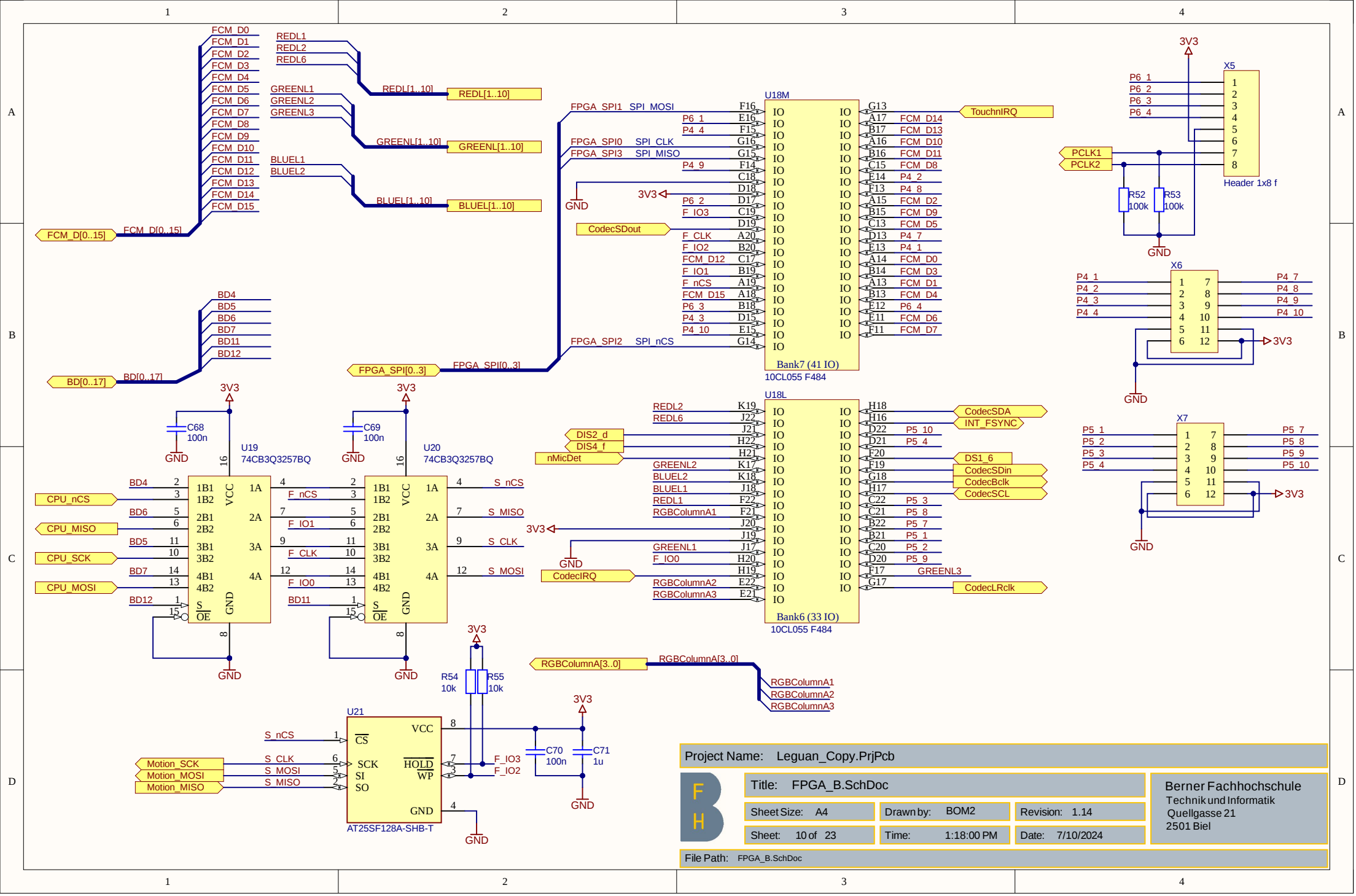
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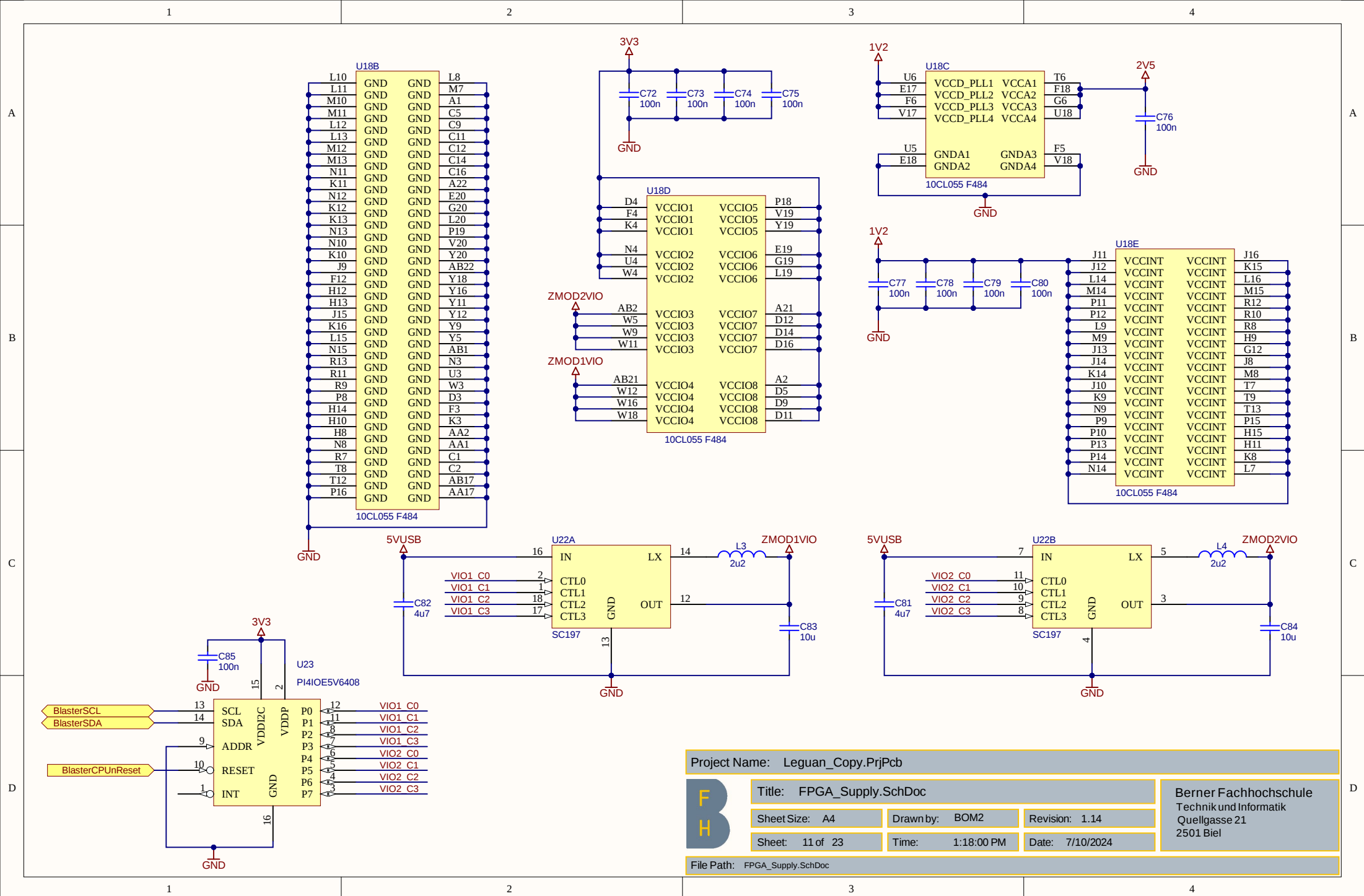
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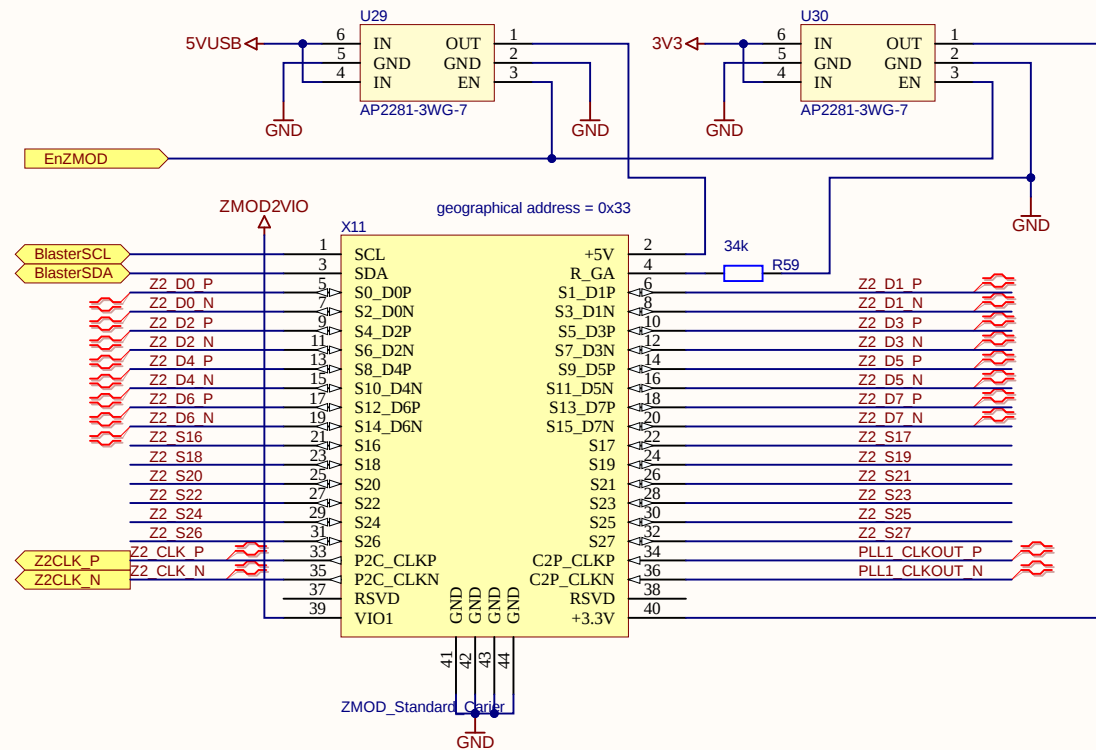
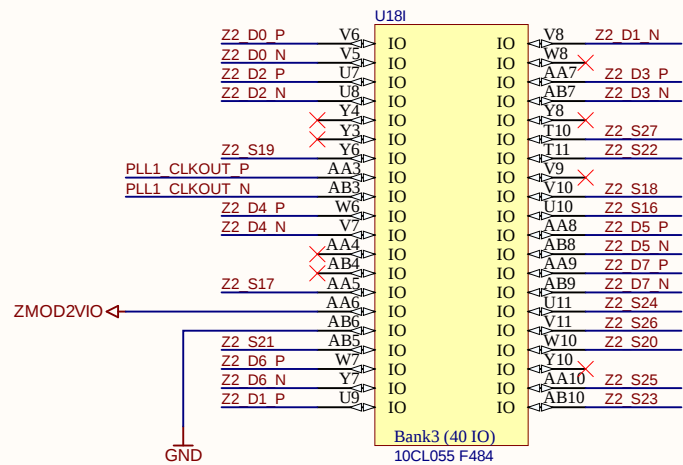
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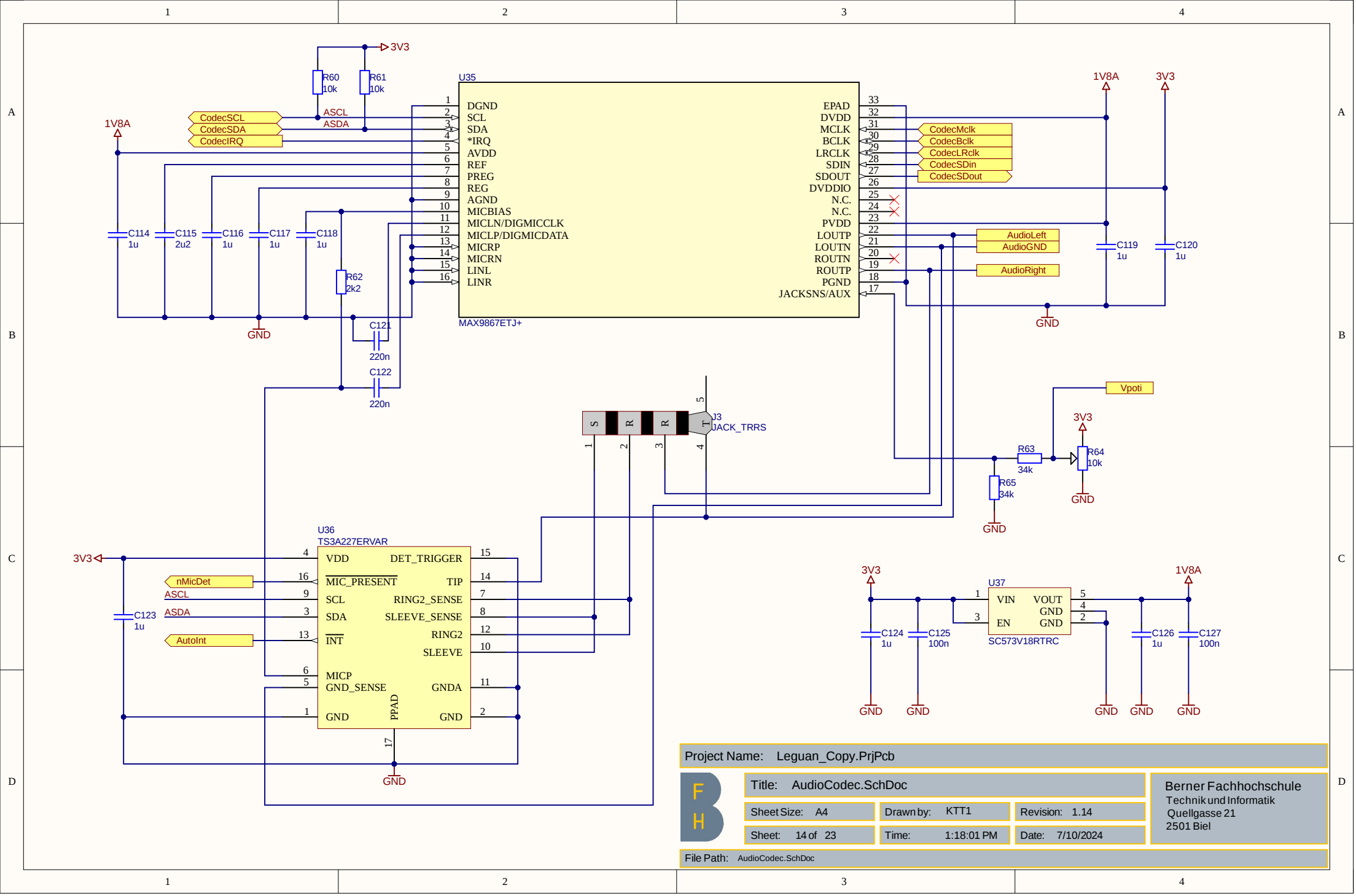
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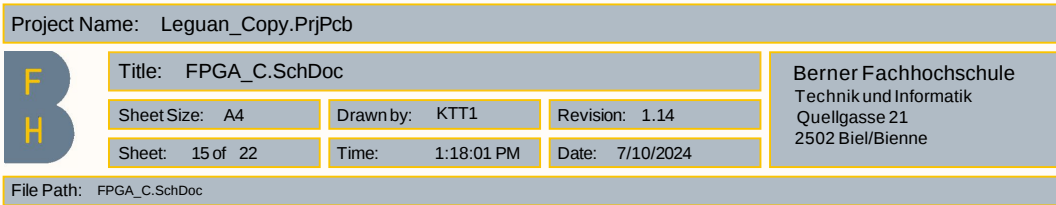
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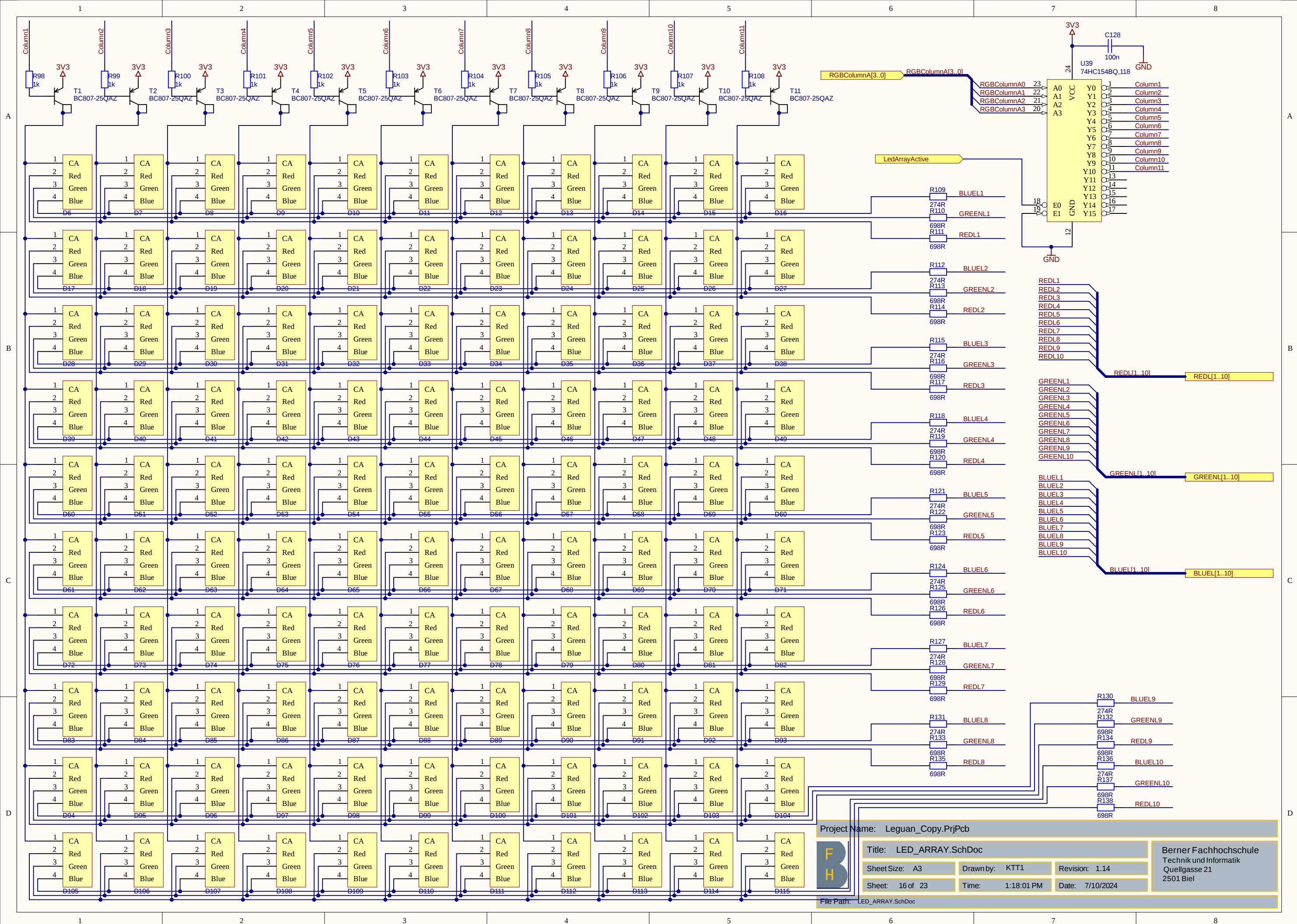


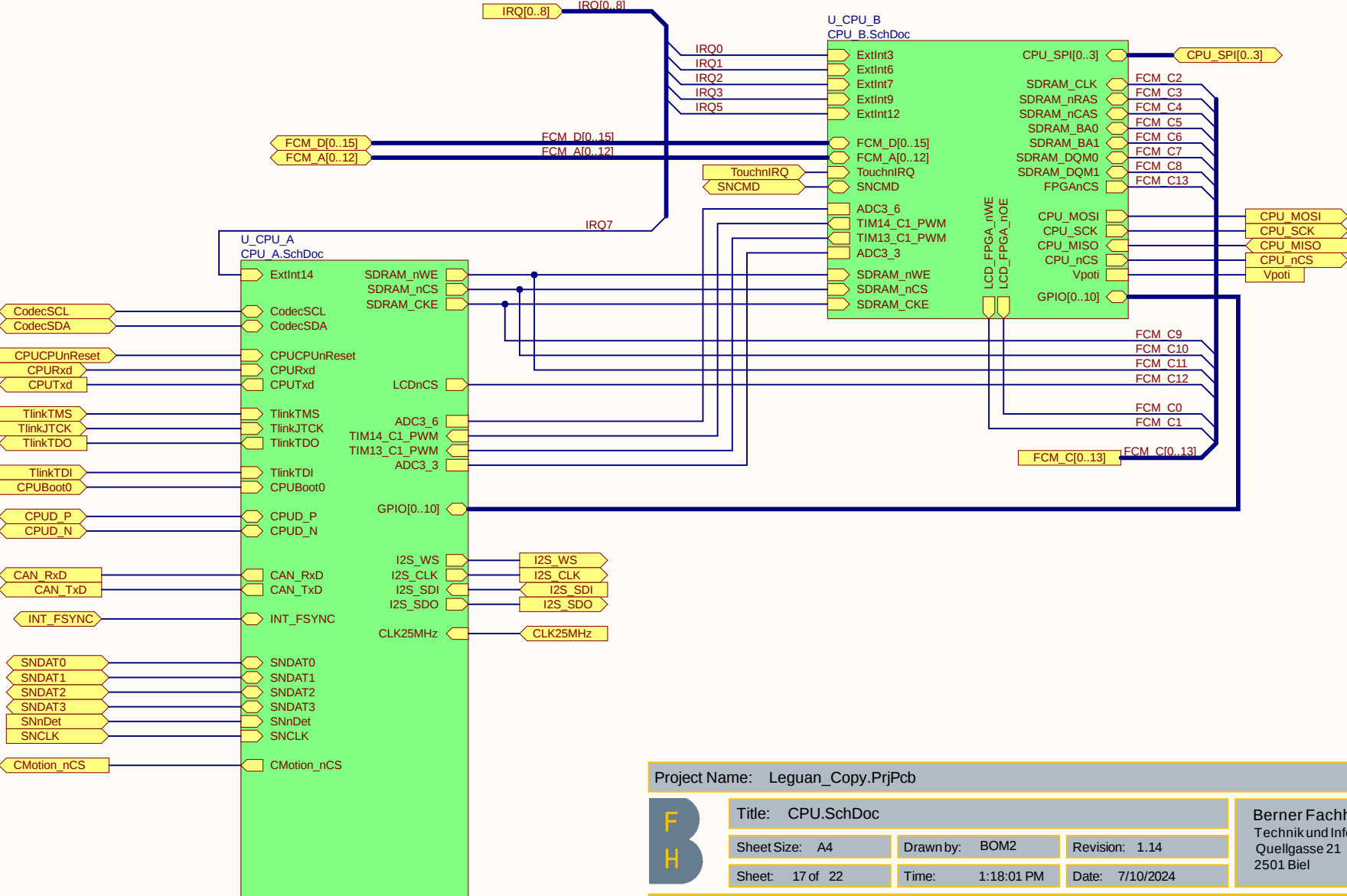












Project Name: Leguan_Copy.PrjPcb

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Title: CPU.SchDoc

Sheet Size: A4

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Time: 1:18:01 PM

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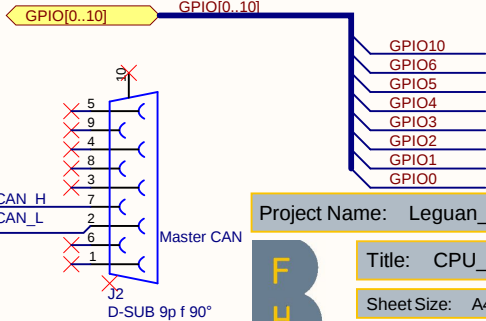
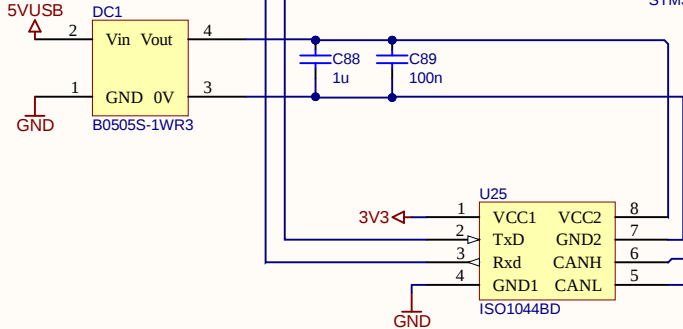
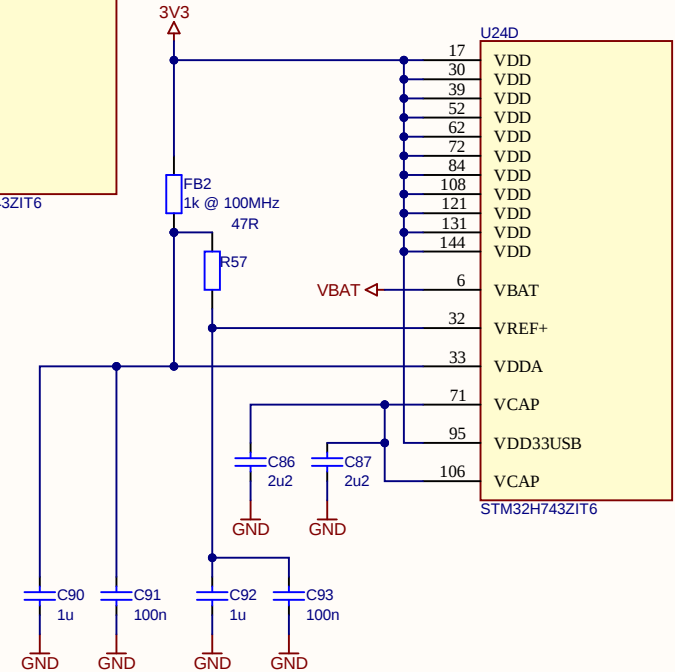
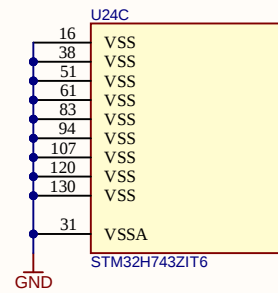
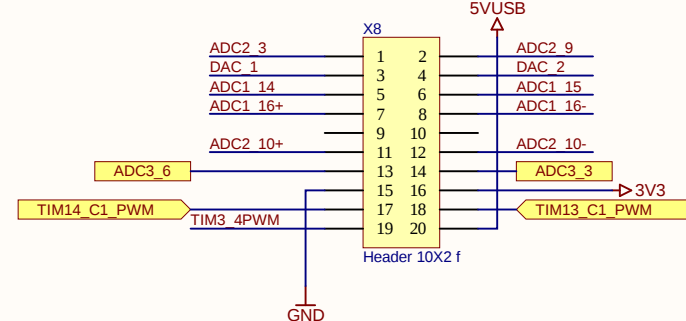
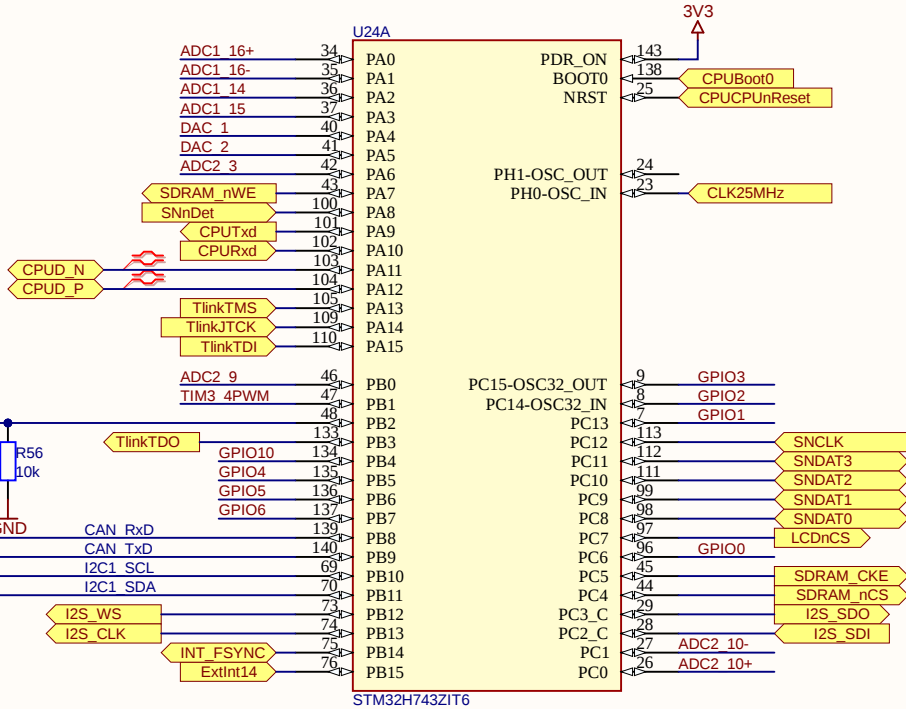
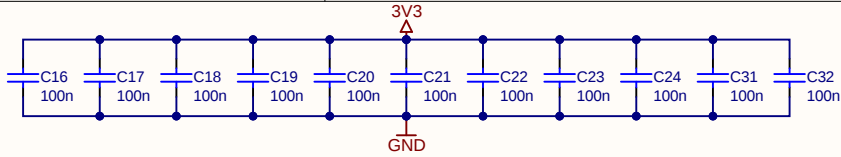
Berner Fachhochschule

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Quellgasse 21

2501 Biel

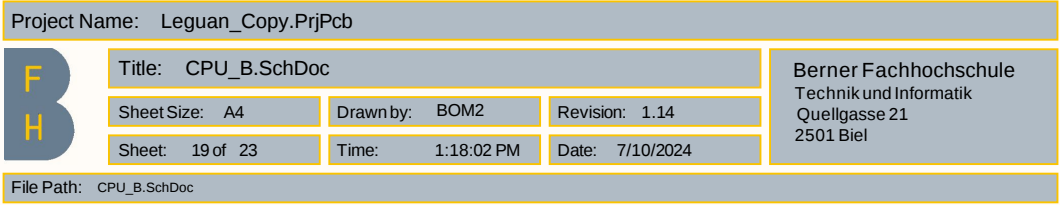
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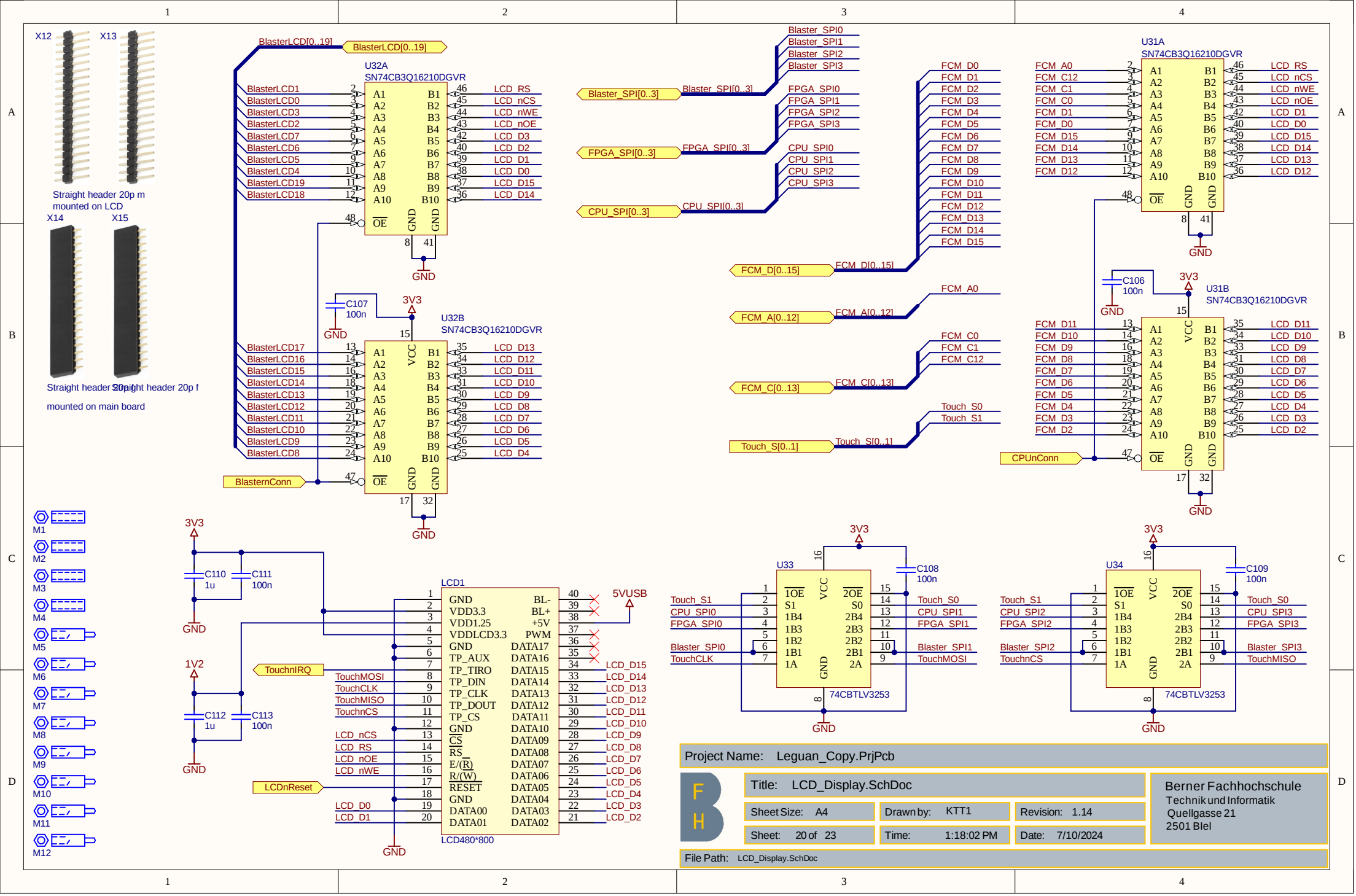


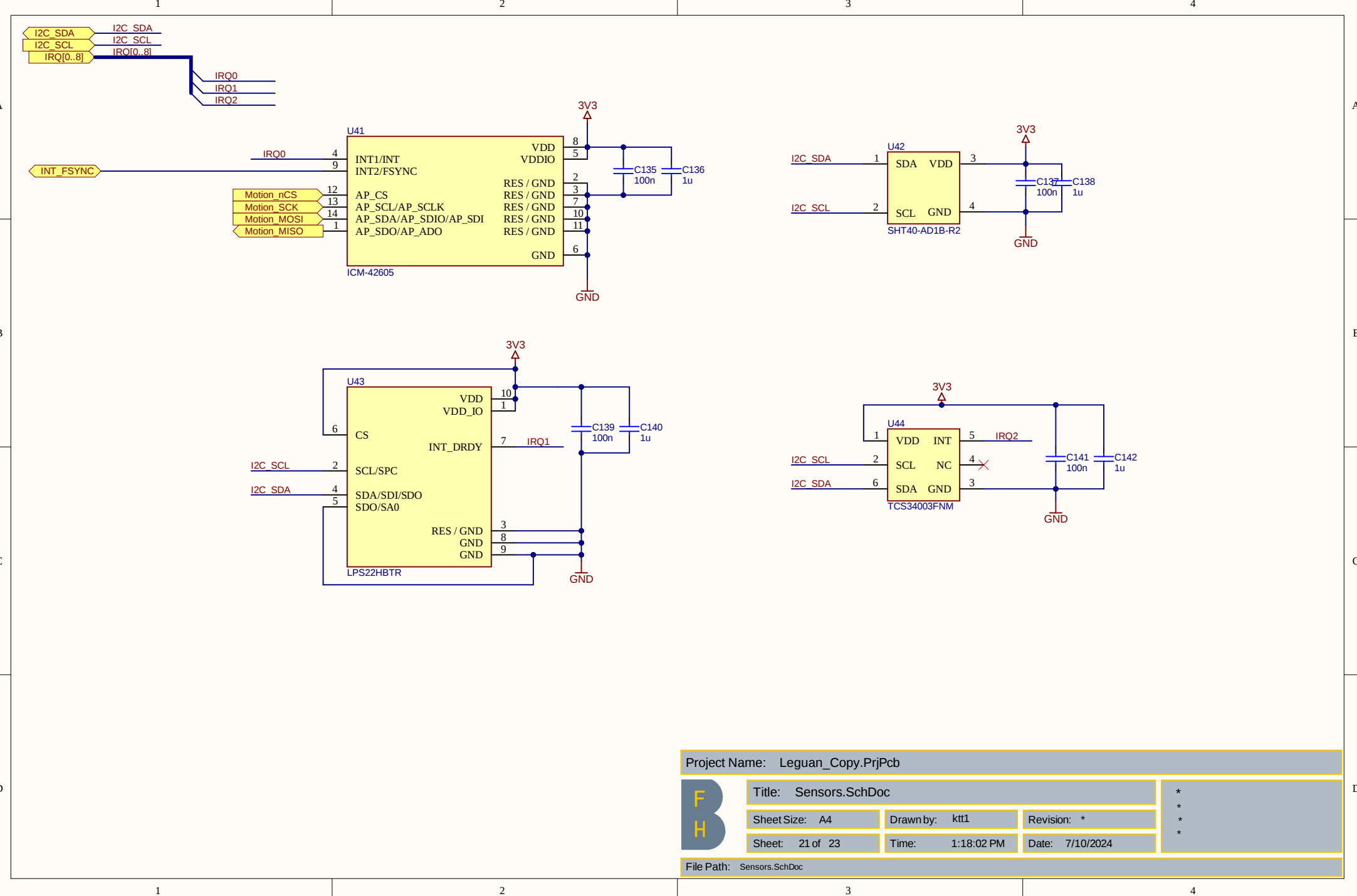
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F H	Title: CPU_A.SchDoc		Berner Fachhochschule Technik und Informatik Quellgasse 21 2501 Biel	
	Sheet Size: A4	Drawn by: BOM2		Revision: 1.14
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File Path: CPU_A.SchDoc







Project Name: Leguan_Copy.PrjPcb

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Title: Sensors.SchDoc

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File Path: Sensors.SchDoc

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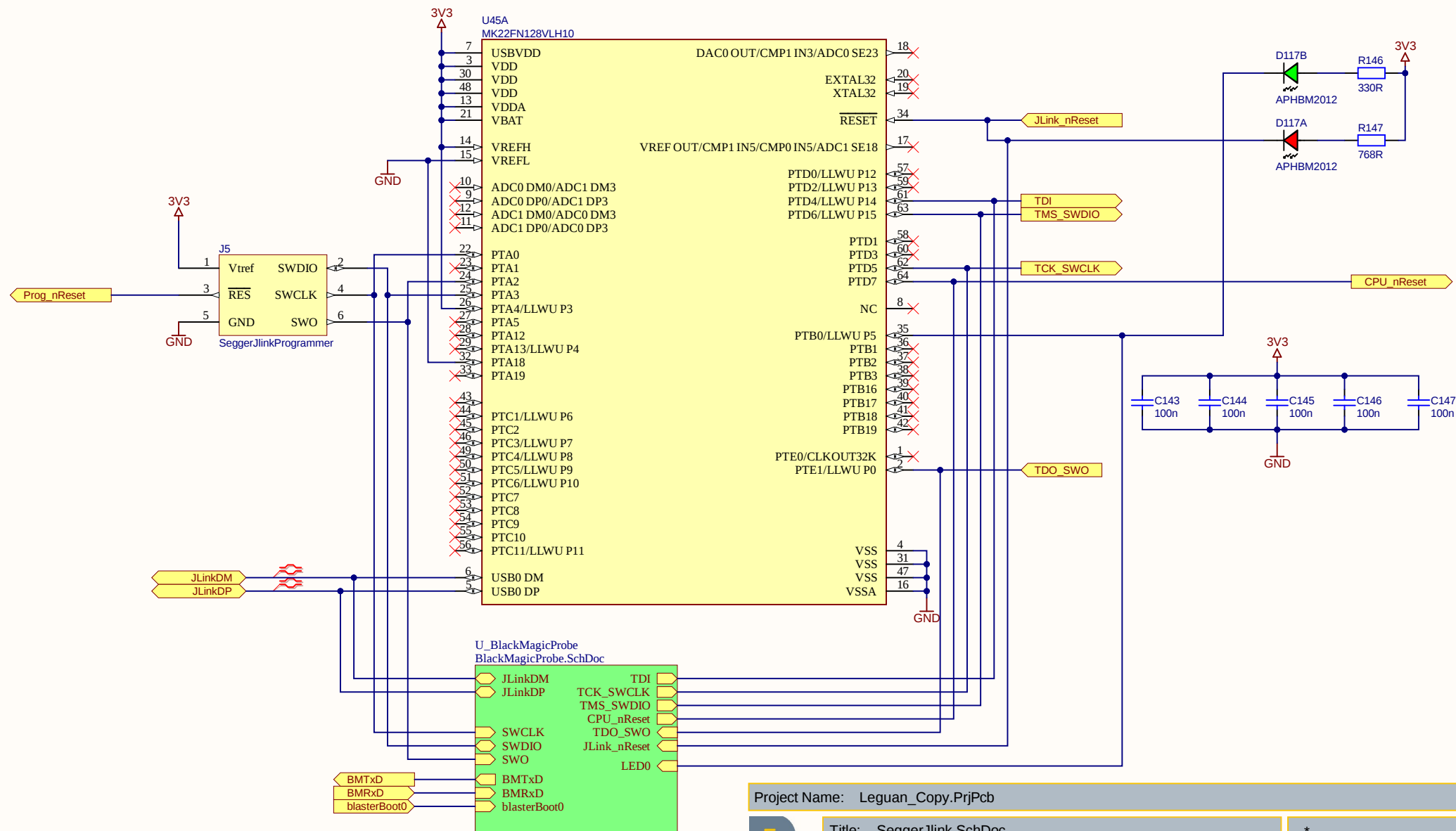
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Project Name: Leguan_Copy.PrjPcb

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Title: SeggerJlink.SchDoc

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Drawn by: *

Revision: *

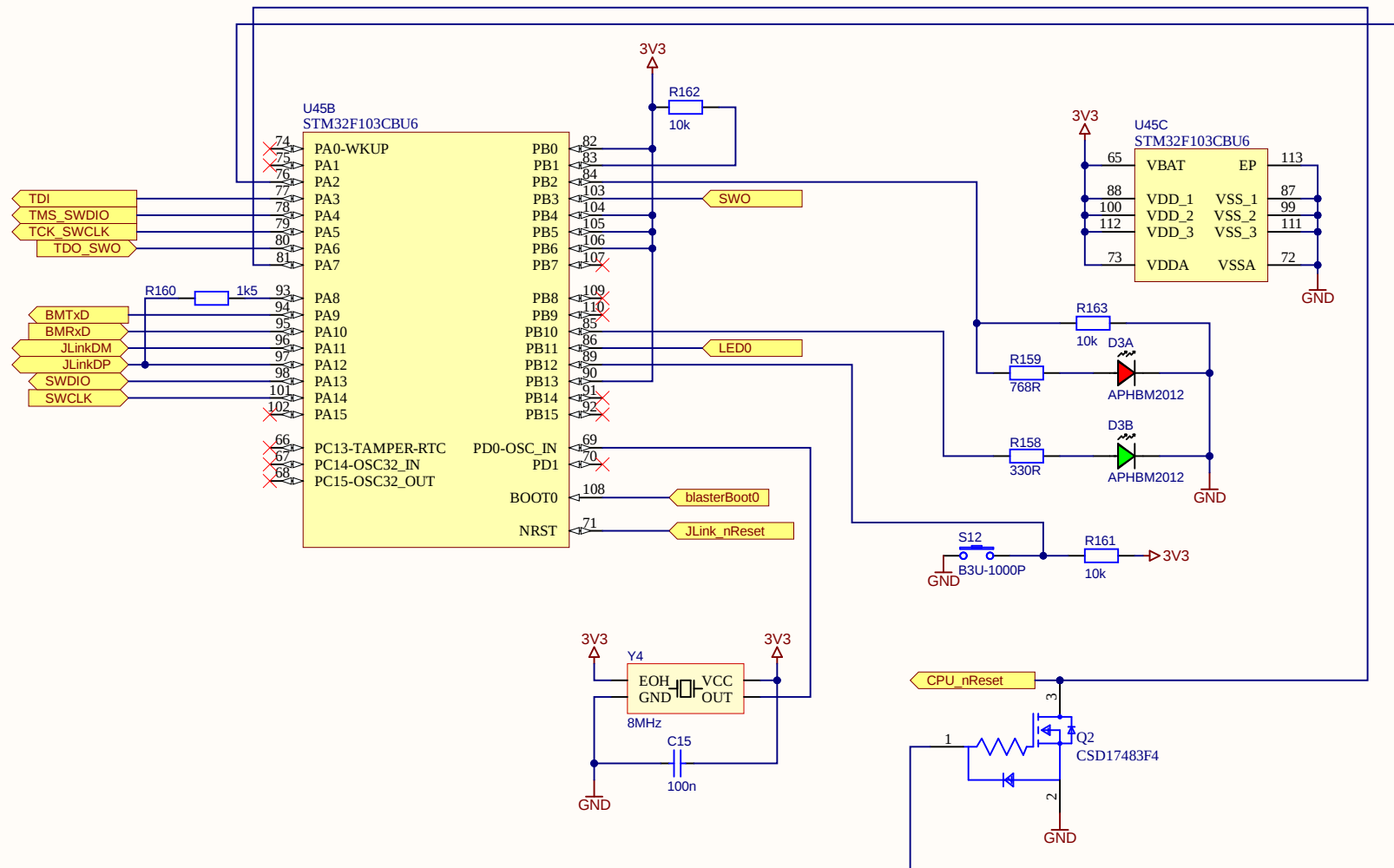
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Date: 7/10/2024

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File Path: SeggerJlink.SchDoc



Project Name: Leguan_Copy.PrjPcb

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Title: BlackMagicProbe.SchDoc

Sheet Size: A4

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Revision: *

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Date: 7/10/2024

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File Path: BlackMagicProbe.SchDoc